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PCI Express devices communicate via a logical connection called an interconnect or link. A link is a point-to-point communication channel between two PCI Express ports allowing both of them to send and receive ordinary PCI requests (configuration, I/O or memory read/write) and interrupts (INTx, MSI or MSI-X).

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### **HDMI Wikipedia**

HDMI (High-Definition Multimedia Interface) is a proprietary audio/video interface for transmitting  
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compliant source device, such as a display controller, to a compatible computer monitor, video  
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into doubt after a wind turbine comes apart after just 8 years of operation.

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### **Petri Netz Wikipedia**

Als Petri-Netze werden Modelle diskreter, vorwiegend verteilter Systeme bezeichnet. Der Informatiker  
Carl Adam Petri hat sie in den 1960er Jahren ausgehend von endlichen Automaten entwickelt, zun  
chst noch nicht in der heute gebr uchlichen Form.

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### **Phylogeny Programs continued University of Washington**

Gary Olsen, of the Department of Microbiology, University of Illinois, Urbana, Illinois (gary (at)  
life.uiuc.edu) has developed a speeded-up replacement for my program DNAML coded in C, called  
fastDNAmI version 1.2.2.

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### **Vulnerability Summary for the Week of March 5 2018 US CERT**

The US-CERT Cyber Security Bulletin provides a summary of new vulnerabilities that have been recorded by the National Institute of Standards and Technology (NIST) National Vulnerability Database (NVD) in the past week.

<http://submit-url.co/Vulnerability-Summary-for-the-Week-of-March-5--2018-US-CERT.pdf>

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